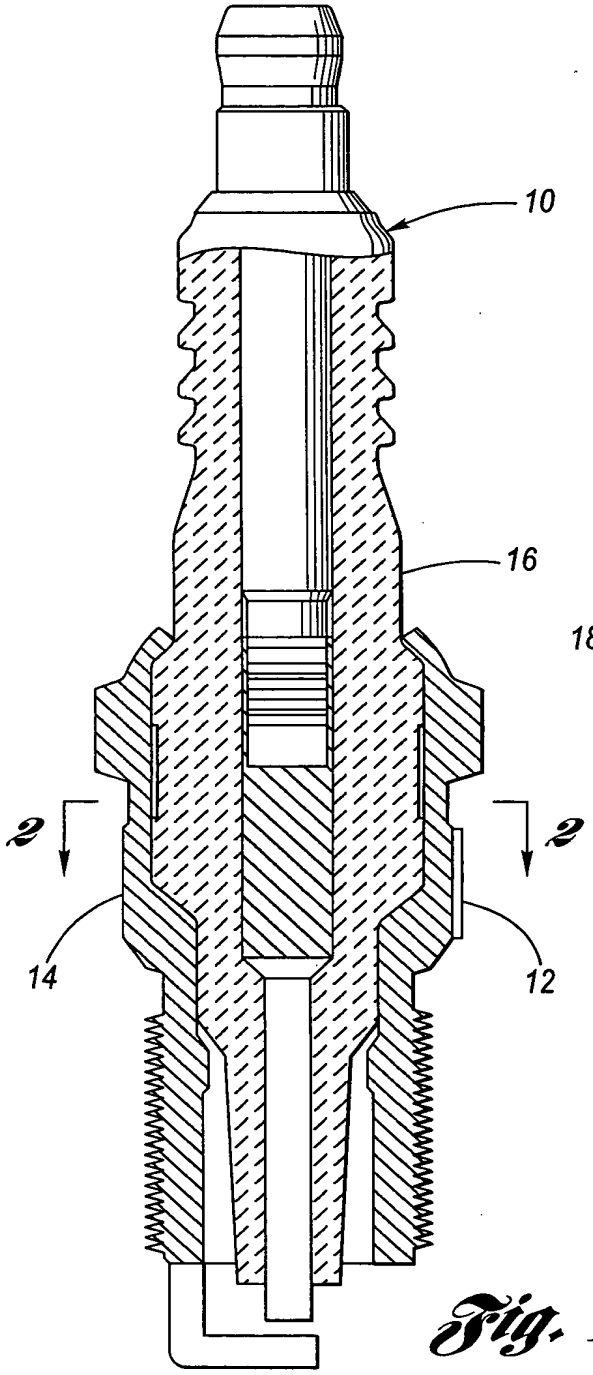

Fig. 1

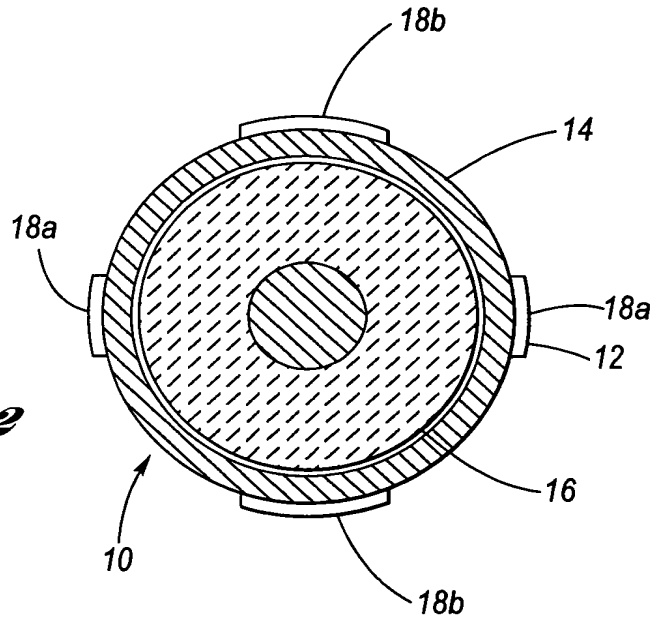

Fig. 2

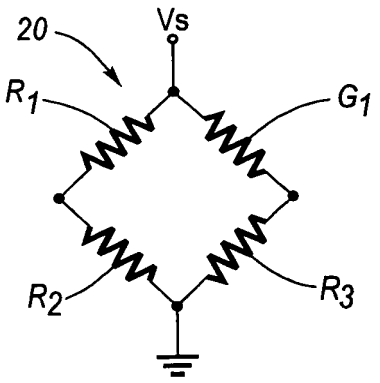

Fig. 3A

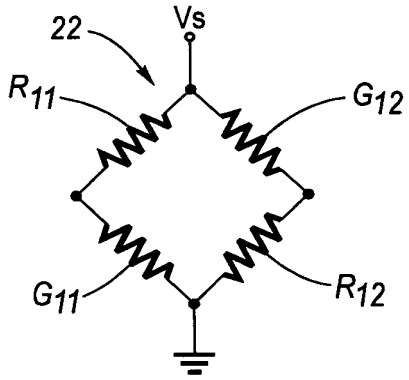

Fig. 3B

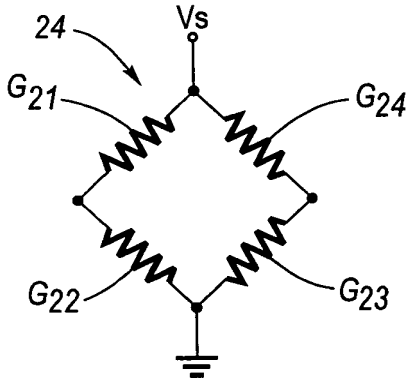

Fig. 3C

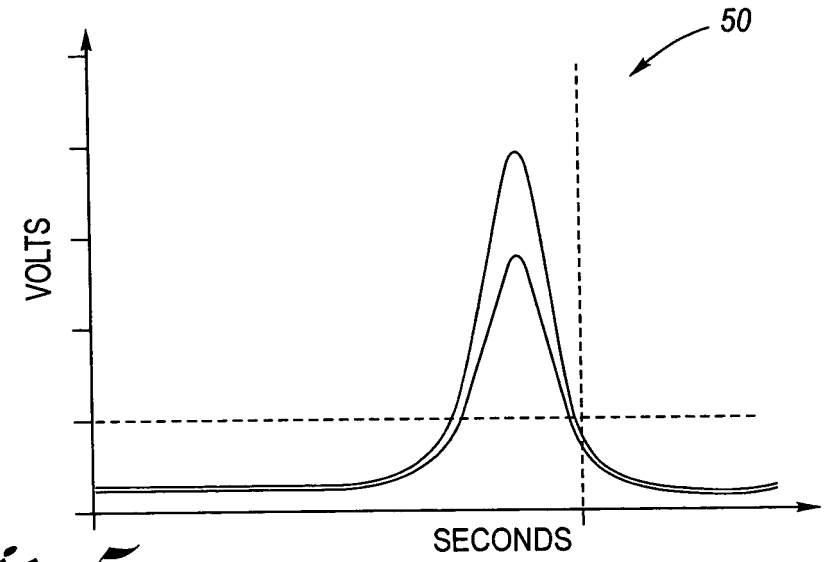

Fig. 5

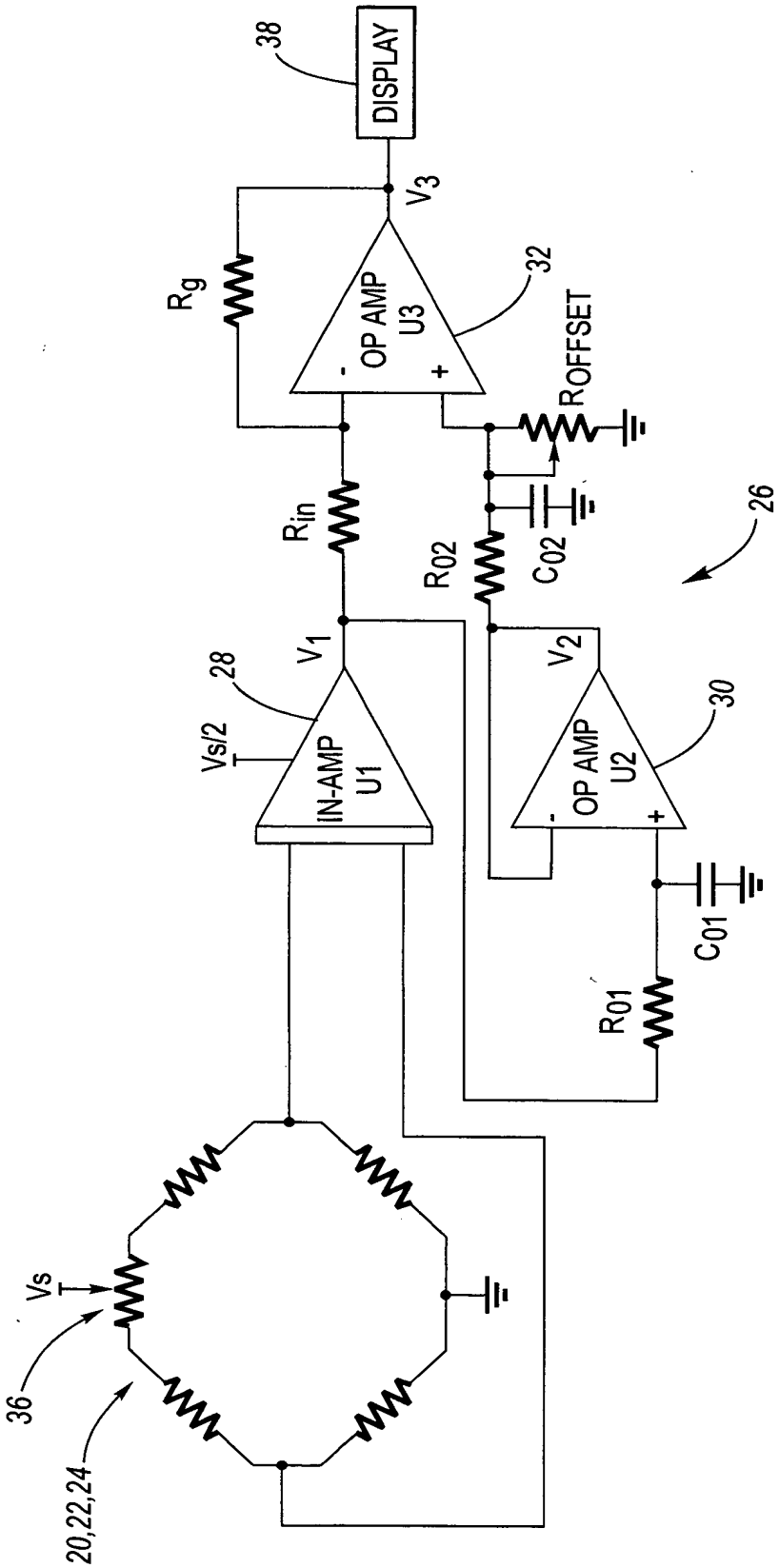
38
DISPLAY
V3
Rg
OP AMP
U3
32
ROFFSET
Rin
R02
C02
26
V1
V2
28
Vs/2
IN-AMP
U1
OP AMP
U2
30
C01
R01
Vs
36
20,22,24

Fig. 4A

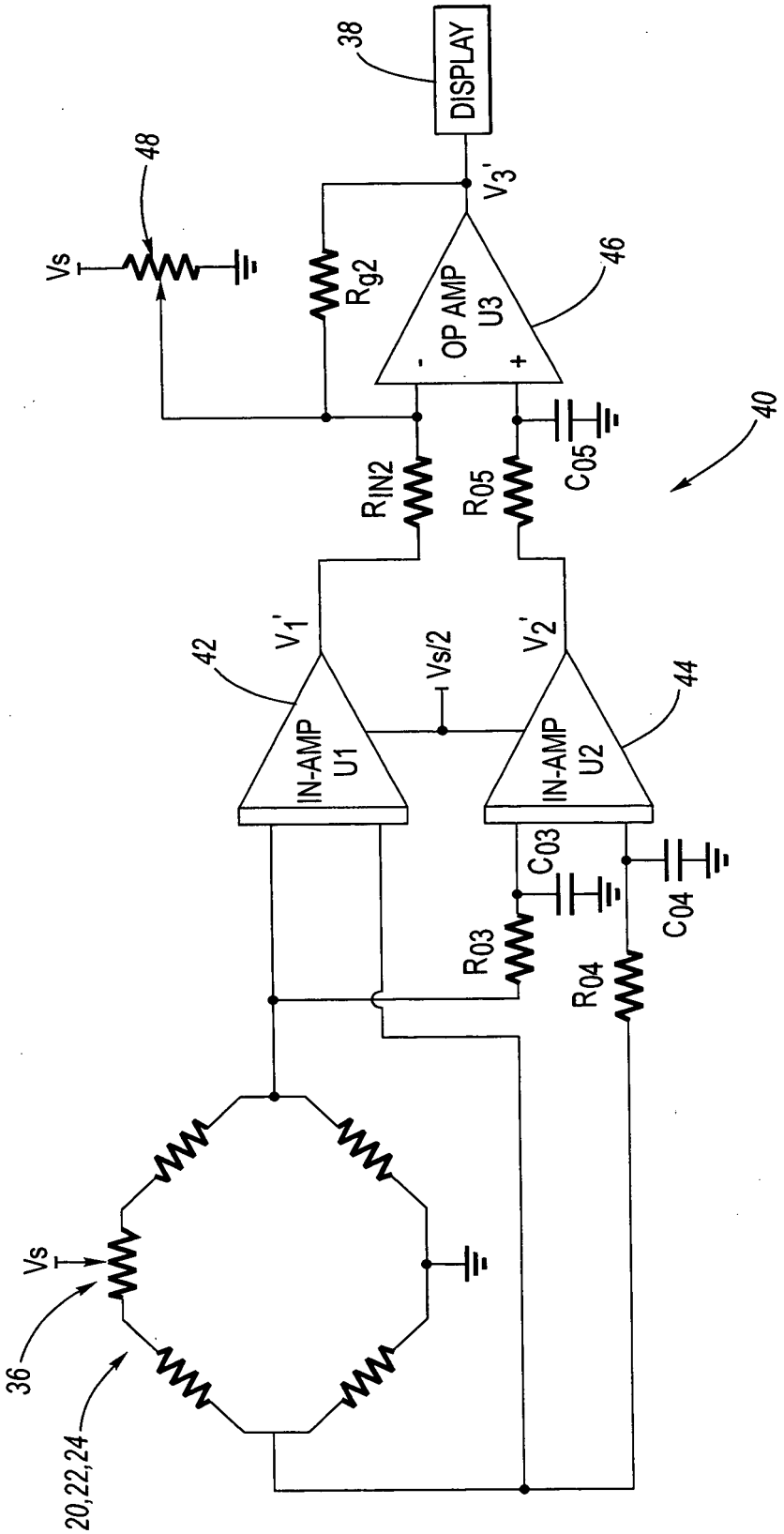
38
DISPLAY
48
Vs
Rg2
V3'
OP AMP
U3
46
40
RIN2
R05
C05
V1'
Vs/2
V2'
42
IN-AMP
U1
IN-AMP
U2
44
C03
C04
R03
R04
Vs
36
20,22,24

Fig. 4B